

FIG. 1 (PRIOR ART)

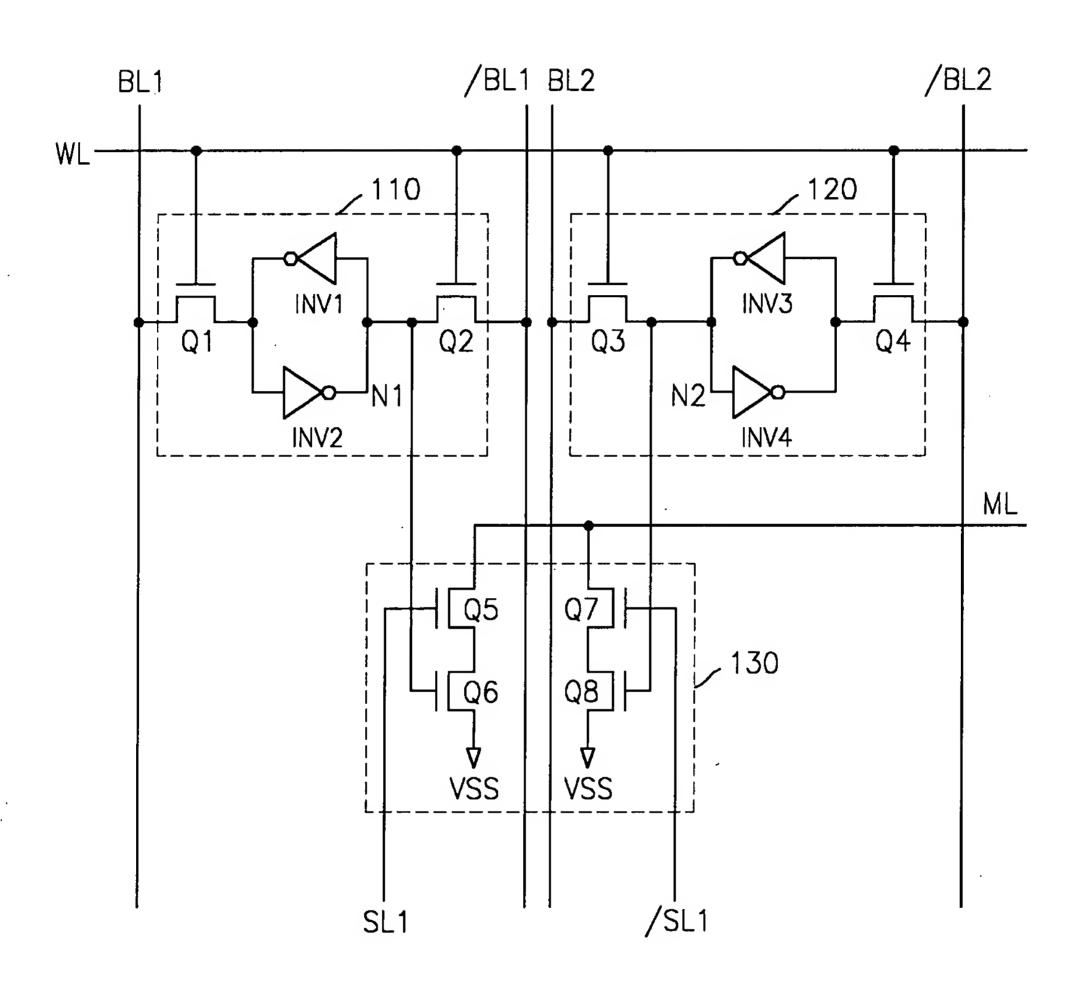
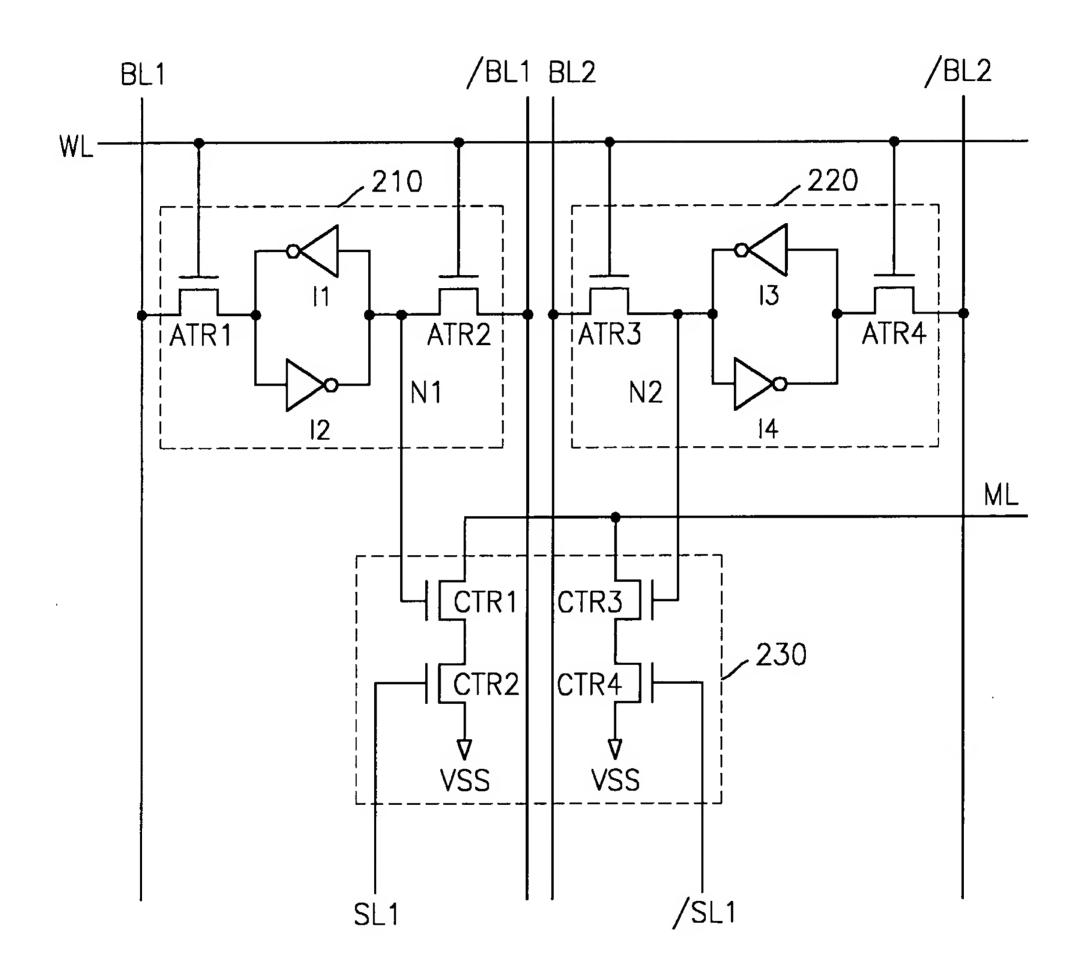


FIG. 2



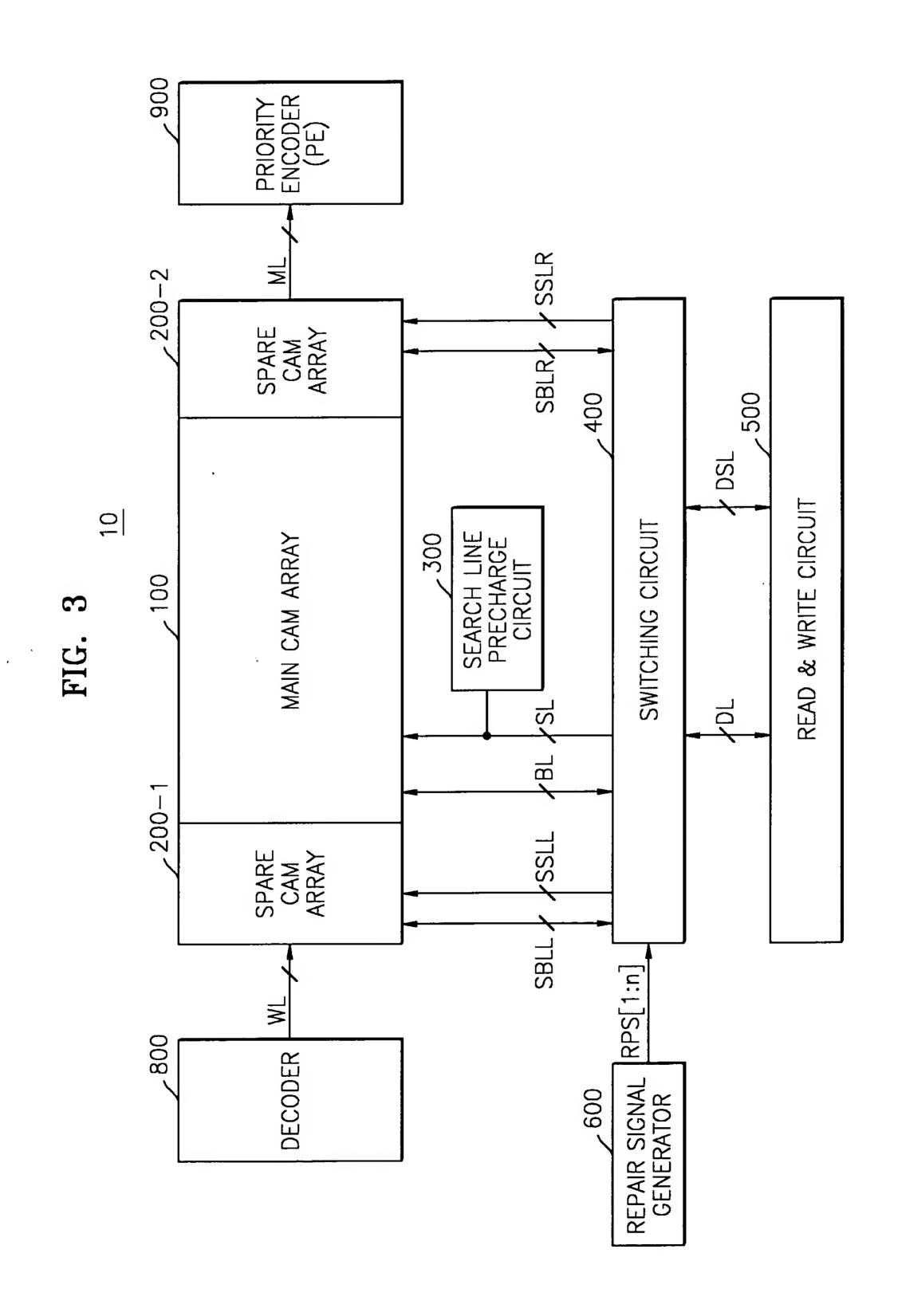
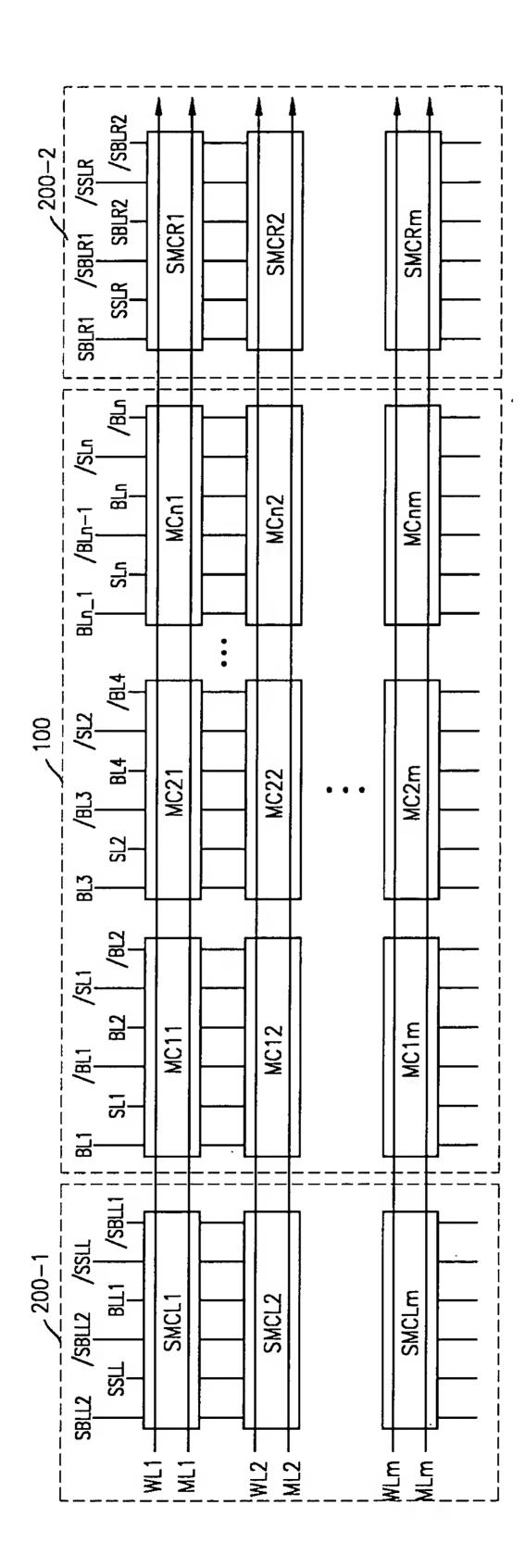
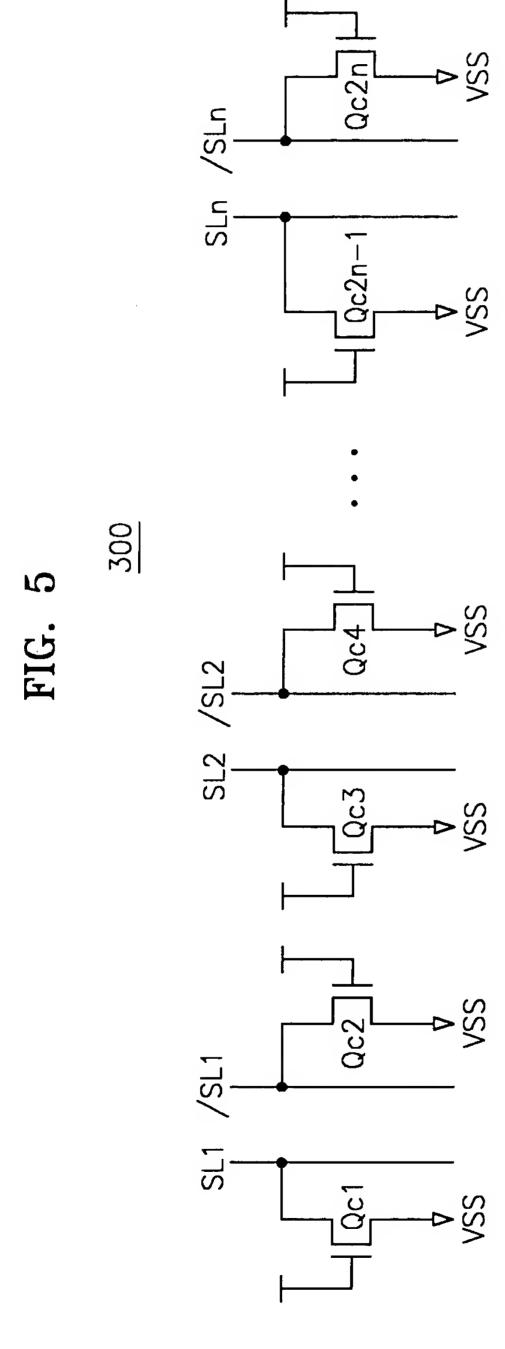
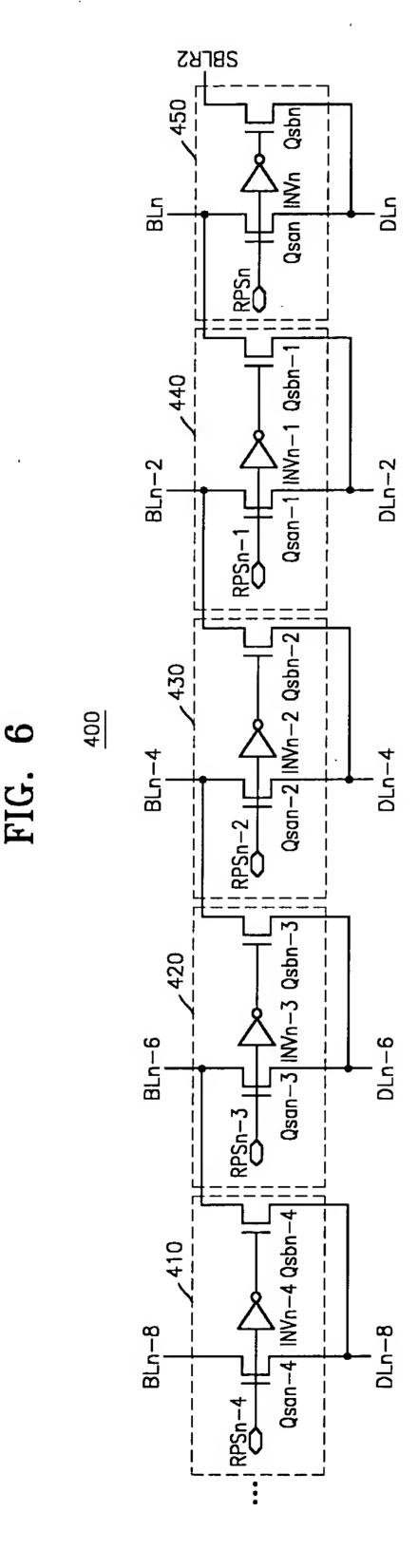
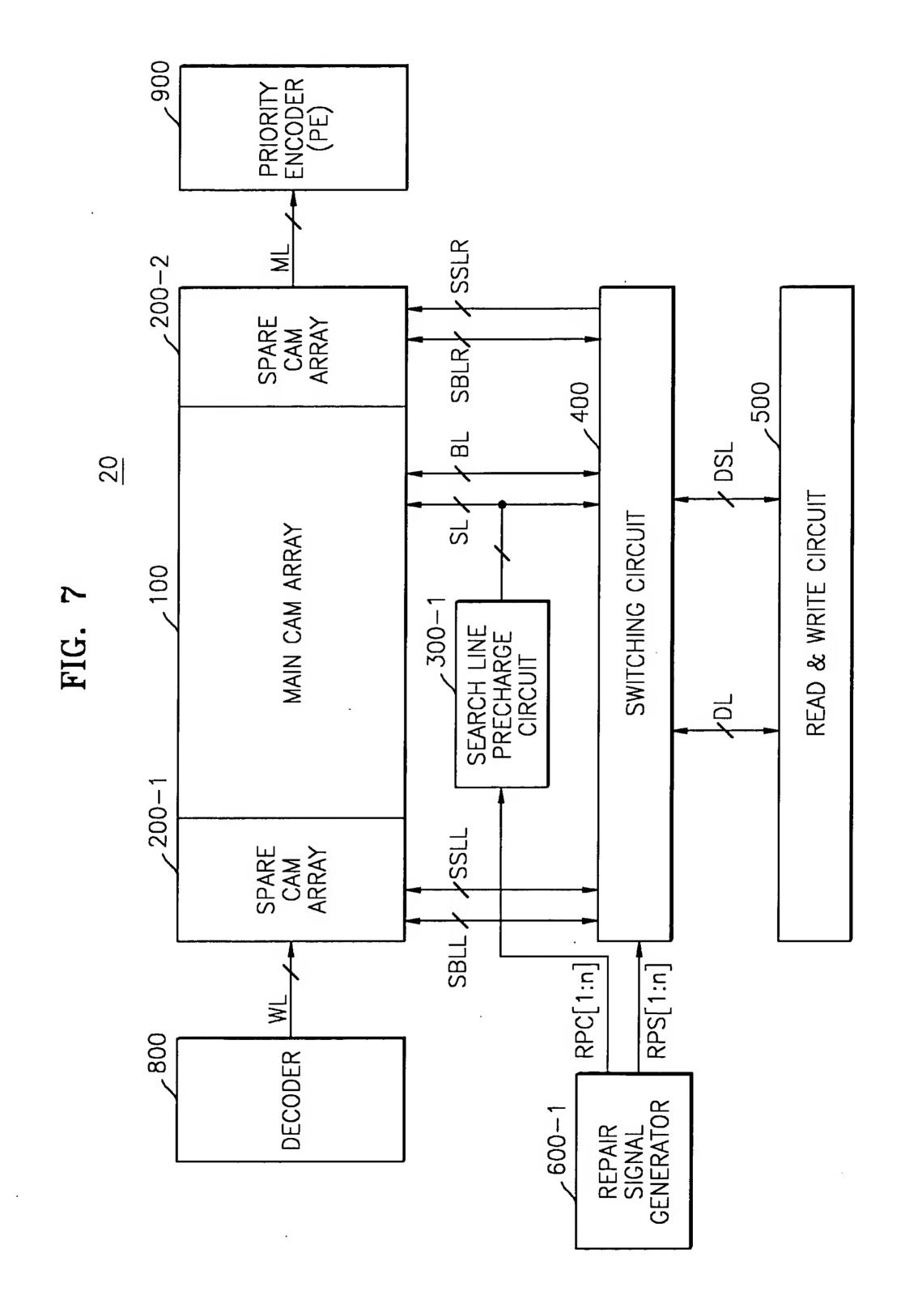


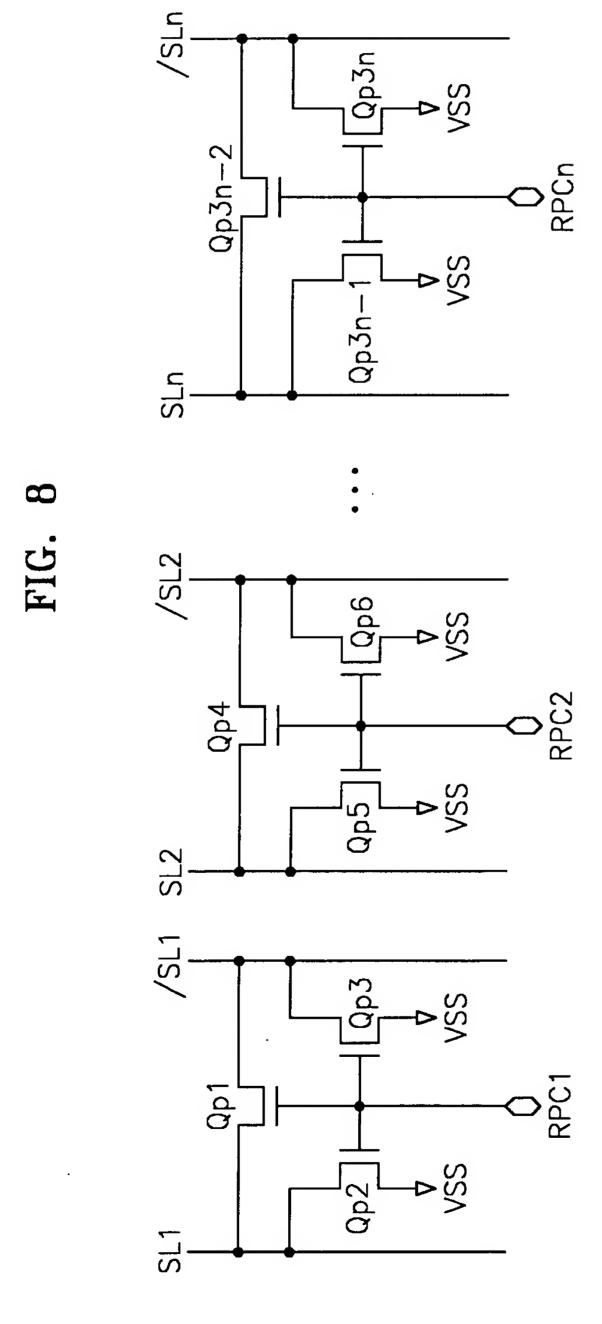
FIG 4

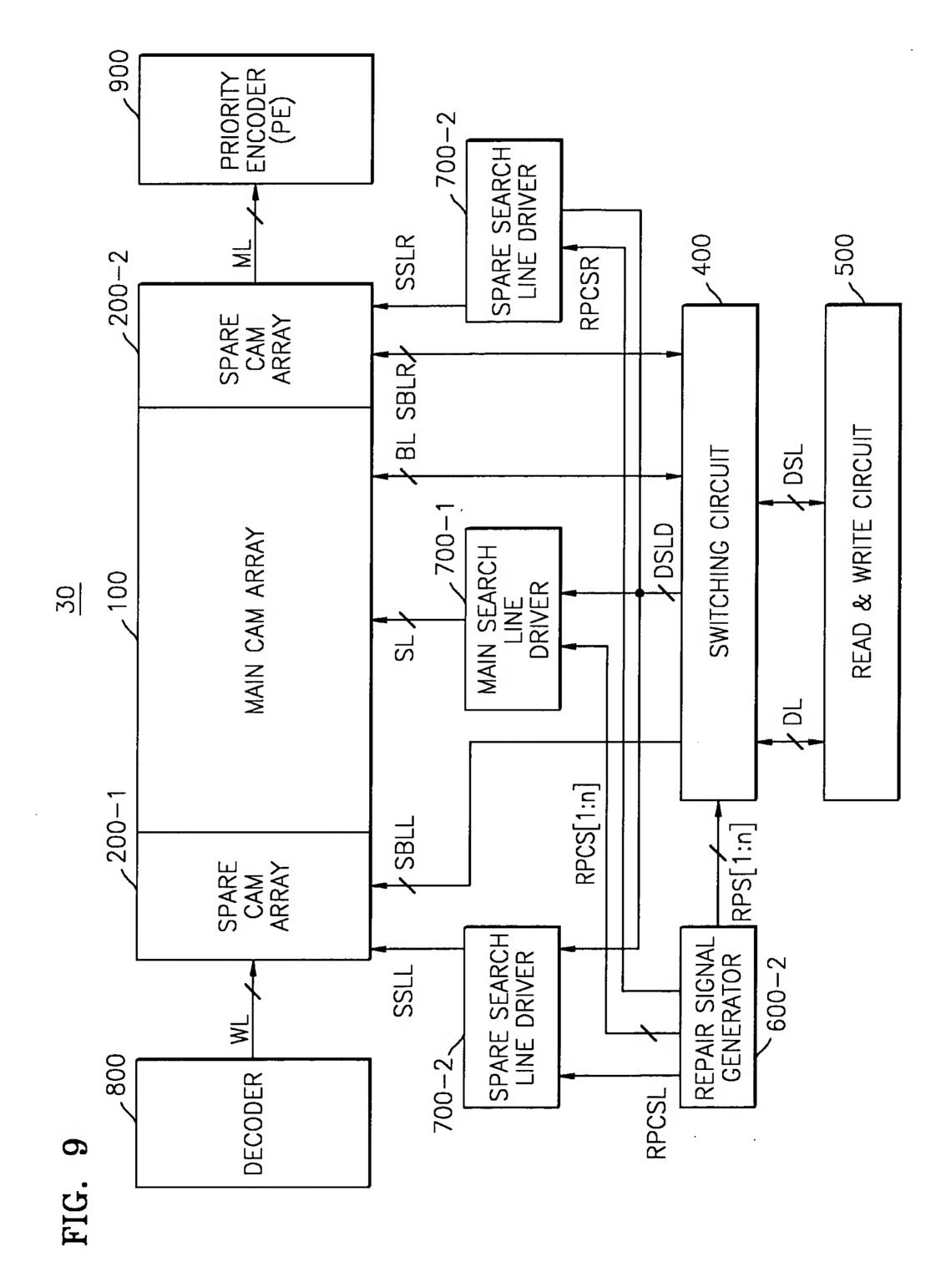












> RPCSn /NANDn 730 /SSLn /DSLDn NANDn MIn DSLDn > RPCS2 /NAND2 720 /SSL2 /DSLD2 NAND2 MI2 DSLD2 > RPCS1 /NAND1 710 /SSL1 /DSLD1 NAND1 SSL1

FIG. 10A

FIG. 10B

